52. Noll, R. Reforming Regulation. The Brookings Inst., Washington,
D.C., 1971.
53. Parker, D.B. Crime by Computer. Scribners, New York, 1976.
54. Peterson, H.E., and Turn, R. System implications of information privacy. Proc. AFIPS 1967 SJCC, Vol. 30, AFIPS Press, Montvale,
N.J., pp. 291-300.
55. Portway, P.S. EFT systems? No thanks, not yet. Computerworld 12, 2 (Jan. 9, 1978), 14-16, 21, 23-25.
56. Privacy Protection Study Commission. Personal Privacy in an Information Society. U.S. Gov't. Printing Office, Washington, D.C., July, 1977.
57. Prives, D. The explosion of state laws on electronic fund transfer systems. P-76-1, Prog. Inform. Technologies and Public Policy, Harvard U., Cambridge, Mass., 1976.
58. Reid, S. The New Industrial Order: Concentration Regulation and Public Policy. McGraw-Hill, New York, 1976.
59. Richardson, D.W. Electric Money: Evolution of an Electronic Funds-Transfer System. M.I.T. Press, Cambridge, Mass., 1970. 60. Rose, $S$. More bang for the buck: The magic of electronic banking. Fortune 95, 5 (1977), 202-226.
60. Rossman, L.W. Financial industry sees EFT privacy laws adequate. American Banker CXLI, 210 (Oct. 28, 1976), 1, 11. 62. Rule, J. Private Lives and Public Surveillance. Schocken Books, New York, 1974.
61. Rule, J. Value Choices in Electronic Funds Transfer Policy. Office of Telecommunications Policy, Executive Office of the President, Washington, D.C., Oct. 1975.
62. Saltzer, J., and Schroeder, M. The protection of information in computer systems. Proc. IEEE 65, 9 (Sept. 1975), 1278-1308.
63. Sayre, K., Ed. Values in the Electric Power Industry. U. of Notre Dame Press, Notre Dame, Ind., 1977.
64. Schick, B. Some impacts of electronic funds transfer on consumer transactions. Federal Reserve Bank of Boston. The economics of a national electronics funds transfer system. Conf. Ser. No. 13, Boston, Mass., Oct. 1974, pp. 165-179.
65. Schuck, P.H. Electronic funds transfer: A technology in search of a market. Maryland Law Review 35, 1 (1975), 74-87.
66. Schultze, L. The public use of the private interest. Harpers 254, 1524 (May 1977), 43-62.
67. Simpson, R.C. Money transfer services. Computers and Society 7, 4 (Winter 1976), 3-9.
68. Steifel, R.C. A checkless society or an unchecked society? Computers and Automation 19 (Oct. 1970), 32-35.
69. Sterling, T., and Laudon, K. Humanizing information systems. Datamation 22, 12 (Dec. 1976), 53-59.
70. The time is NOW. Forbes Magazine 120, 1 (July 1, 1977), 61-62. 73. Turoff, M., and Mitroff, I. A case study of technology assessment applied to the "cashless society" concept. Technol. Forecasting Soc., Change 7 (1975), 317-325.
71. Weissman, C. Secure computer operation with virtual machine partitioning. Proc. AFIPS 1975 NCC, Vol. 44, AFIPS Press, Montvale, N.J., 1975, pp. 929-934.
72. U.S. Dept. HEW, Secretary's Advisory Committee on Automated Personal Data Systems. Records Computers, and the Rights of Citizens. Washington, D.C., 1973.
73. Walker, G. M. Electronic funds transfer systems. Electronics (July 24, 1975), 79-85.
74. Webber, M. The BART experience-What have we learned? The Public Interest, Vol. 45 (Fall 1976), 79-108.
75. Weizenbaum, J. Computer Power and Human Reason. Freeman, San Fransisco, 1976.
76. Wessel, M. Freedom's Edge: The Computer Threat to Society. Addison-Wesley, Reading, Mass., 1974.
77. Westin, A., and Baker, M. Databanks in a Free Society. Quadrangle Books, New York, 1972.
78. Whiteside, T. Computer Capers. Crowell, New York, 1978.
79. Wilcox, C., and Shepard, W. Public Policies Towards Business.

Richard D. Irwin, Homewood, Fifth ed., 1975.
83. Winner, L. Autonomous Technology: Technology Out-of-Control as a Theme in Political Thought. M.I.T. Press, Cambridge, Mass., 1977.
84. Wise, D. The American Police State. Random House, New York, 1976.

Programming Techniques
S. L. Graham, R. L. Rivest Editors

# Fast Parallel Sorting Algorithms 

D. S. Hirschberg<br>Rice University


#### Abstract

A parallel bucket-sort algorithm is presented that requires time $O(\log n)$ and the use of $n$ processors. The algorithm makes use of a technique that requires more space than the product of processors and time. A realistic model is used in which no memory contention is permitted. A procedure is also presented to sort $n$ numbers in time $O(k \log n)$ using $n^{1+1 / k}$ processors, for $k$ an arbitrary integer. The model of computation for this procedure permits simultaneous fetches from the same memory location.


Key Words and Phrases: parallel processing, sorting, algorithms, bucket sort

CR Categories: 3.74, 4.34, 5.25, 5.31

There is often a time-space tradeoff in serial algorithms. In order to solve a problem within a certain time bound, a minimal amount of space is required. This space requirement may be reduced if we allow more time for the process. In the limit, there will be a minimum amount of space required.

Much work has recently been devoted to developing algorithms for parallel processors. Problem areas include sorting $[3,6,16,17]$, evaluation of polynomials, and general arithmetic expressions [14, 4], and matrix- and graph theoretic problems [15, 5, 2, 12, 9]. In parallel algorithms, there is a similar tradeoff between time and the number of processors used. In order to solve a problem using a bounded number of processors, a minimal amount of time is required. This time requirement

[^0]may be reduced if we allow more processors to help in the computation. In the limit, there will be a minimum time requirement.

We shall present an algorithm design technique which dramatically exemplifies the three-way tradeoff between space, time, and processors. It is hoped that this technique can also be applied to strictly serial algorithms.

Muller and Preparata [13] were the first to exhibit a network capable of sorting $n$ numbers in time $O(\log n)$. Their method required $O\left(n^{2}\right)$ processing elements. We shall present an algorithm for sorting $n$ numbers in time $O(\log n)$ that requires asymptotically fewer processors.

We first present parallel bucket-sorting algorithms in which, at the expense of greater space requirements, the number of processors and the amount of time used are both reduced. The algorithms are unusual in that the space requirements are greater than the processor time requirements.

Our computational model assumes that all processors have access to a common memory as well as having small local memories. All processors are synchronized and follow the instructions of a unique instruction stream. This model has been called an SIMD (Single Instruction stream, Multiple Data stream) computer [7]. The instructions may involve memory references or constants that are a linear function of the bits in the binary representation of the processor number, the processors being numbered by consecutive integers starting from zero. We assume that the addition of two numbers can be executed in one time unit.

Our preliminary algorithm will sort $n$ numbers using $n$ (parallel) processors in time $O(\log n)$ under the assumption that the numbers that are to be sorted, $\left\{c_{i}\right\}_{i=1}^{n}$, are from $\{0,1, \ldots, m-1\}$ and with the proviso that duplicate numbers (should there be any) are to be discarded. The proviso will be dropped in the second algorithm. This is a parallel version of the "bucket sort."

An obvious implementation of the parallel bucket sort would be for each processor $p_{i}$ (which has temporarily been "assigned to" $c_{i}$, the ith number being sorted) to place the value $i$ in bucket $c_{i}$. The problem with this solution is that, in general, there may be several values of $i$ with identical numbers $c_{i}$. A memory conflict would result from the simultaneous attempts of several processors to store different values of $i$ into the same bucket.

Our answer to this problem is to eliminate duplicate copies of the same number. Processor $p_{i}$ will be (temporarily) deactivated if there is another processor $p_{i}$ whose index, $j$, is smaller than $i$, and $c_{j}=c_{i}$. Then, for each number appearing among the numbers being sorted, only one processor (the one with smallest index) will be active when we place $i$ in bucket $c_{i}$.

Our implementation of the elimination procedure is interesting. We shall have $m$ areas of memory, one for each bucket. Each area will be of size $n$, the number of input numbers to be sorted. Within each area, $j$, the processors ( $p_{i}$ ) having $c_{i}=j$ will leave marks indicating their presence. Then, in a binary-tree fashion, they will
search for the presence of (the marks of) other active processors. If two processors discover each other's presence (such discoveries will turn out to be simultaneous), the lower ranking one (i.e. the one with smaller index) will continue while the higher ranking one will deactivate.

There being $n$ locations per area (numbered 0 through $n-1$ ), each processor $p_{i}$ can make its mark at location $i$ in area $c_{i}$ without fear of memory conflict. Iteratively, each processor then determines whether or not its "buddy" is active within the same area. (Here, "buddy" is defined analogously to the definition used in the Buddy System for dynamic memory allocation [10].) If so, then the processor with higher rank (i.e. larger index $i$ ) will deactivate. If the buddy is not active or if it is active but is of higher rank, then the processor will continue, shifting its mark to the location of the buddy if that location is of lower index than the one currently in use. If the buddy was active, then, of course, no shift will occur.

After the $k$ th iteration, a mark will be present at each location whose last $k$ bits are zeros and whose other $(\log n)-k$ bits coincide with the corresponding bits of the address of a processor active in the same area. Thus, each such location will be marked iff any of $2^{k}$ processors had been active in that area originally. After $\log n$ iterations, the first location in an area will be marked iff any of the $n$ processors originally were active in that area, i.e. iff any of the $n$ numbers to be sorted was $j$, the area bucket number.

The algorithm is expressed formally below. Variables in capitals are in common memory, variables in lower case are in local memory (i.e. there will be one copy of each such variable for each processor).

```
Algorithm 1-parallel bucket sort
Input: \(0 \leq i \leq n-1 \quad A[j, i]=0\)
        \(0 \leq j \leq m-1 B[j]=0\)
                \(c_{i} \in\{0,1, \ldots, m-1\}\), not necessarily distinct
Output: \(0 \leq i \leq n-1 \quad A[j, i]=0\)
        \(0 \leq j \leq m-1 B[j]=\min i\) s.t. \(c_{i}=j, 0\) if none such
Let \(e_{k}=0 \ldots 010 \ldots 0\), all bits 0 except the \(k\) th from the
right
\begin{tabular}{|c|c|}
\hline \multicolumn{2}{|l|}{for all ido} \\
\hline \multicolumn{2}{|l|}{Let \(i=x_{\log n} \ldots x_{2} x_{1}\) be the binary representation of \(i\)} \\
\hline \(x \leftarrow i\) & \(x\) is the location that \(p_{i}\) is marking \\
\hline \multicolumn{2}{|l|}{\(A\left[c_{i}, x\right] \leftarrow 1\)} \\
\hline flag \(\leftarrow 1\) & flag \(=1\) iff \(p_{i}\) is active \\
\hline \multicolumn{2}{|l|}{for \(k \leftarrow 1\) step 1 until \(\log n\) do begin} \\
\hline buddy \(\leftarrow x \oplus e_{k}\) & address of buddy \\
\hline count \(\leftarrow A\left[c_{i}\right.\), buddy] & count \(\neq 0\) if buddy is active \\
\hline if \(x_{k}=1\) AND count \(\neq 0\) then flag \(\leftarrow 0\) & if buddy is active and if we are the higher of the 2 then we'll wait and buddy will continue \\
\hline if \(x_{k}=1\) AND count \(=0\) AND flag \(=1\) then begin & \\
\hline
\end{tabular}
```

Communications

```
    \(A\left[c_{i}, x\right] \leftarrow 0\)
    \(x \leftarrow\) buddy
    \(A\left[c_{i}, x\right] \leftarrow 1\)
    end
end
if flag \(=1\) then \(B\left[c_{i}\right] \leftarrow i\)
\(A\left[c_{i}, x\right] \leftarrow 0\)
```

    if buddy is not active then there
    It is noted that this bucket-sort algorithm requires space $S=O(m n)$, time $T=O(\log n)$, and the use of $n$ processors.

We now present another bucket-sort algorithm. This algorithm will give the actual ranking of the input numbers, equal numbers being kept in the same relative order but getting different ranks, assuming that the input numbers are from a predefined small set.

The algorithm follows the same basic pattern set by our previous algorithm. However, instead of a simple mark bit, we shall keep a running count of how many processors were originally active in each block of indices of size $2^{k}$. If a processor encounters an active buddy then only the lower buddy continues to be active. In any case, all processors $p_{i}$ (active or not) will add to their running count of the number of processors (that were originally active) having indices greater than i. Active processors keep their count at the head of the largest block that they have investigated (which will be of size $2^{k}$ ). At the end there will be at most one active processor per area and $A[j, 0]$ will be the number of different $i$ 's such that $c_{i}=j$. An inactive processor, $p_{i}$, will keep its count at the head of the largest block which had no other processors of index smaller than $i$.

```
Algorithm 2.1—parallel bucket sort (part 1)
Input: \(\quad 0 \leq i \leq n-1 \quad A[j, i]=0\)
    \(0 \leq j \leq m-1 \quad B[j]=0\)
        \(c_{i} \in\{0,1, \ldots, m-1\}\) not necessarily distinct
Local
Output: \(\quad r=\max k\) s.t. \(\neg \exists t\) in same \(2^{k}\)-block as \(i\)
                                with \(t<i\) and \(c_{t}=c_{i}\)
                            \(y=\) head of \(2^{r}\)-block containing \(i\)
Output: \(0 \leq i \leq n-1 \quad A[j, i]=0\) except \(A\left[c_{i}, y\right]=\#\) of \(t \geq i\) s.t.
                            \(c_{t}=c_{i}\)
        \(0 \leq j \leq m-1 \quad B[j]=\min i\) s.t. \(c_{i}=j, 0\) if none such
for all \(i\) do
    flag \(\leftarrow 1\)
    \(r \leftarrow \log n\)
    \(x \leftarrow i \quad x\) is the head of the largest block
    that \(p_{i}\) has counted
    \(y \leftarrow i\)
\(A\left[c_{i}, y\right] \leftarrow 1\)
for \(k \leftarrow 1\) step 1 until \(\log n\) do
begin
    buddy \(\leftarrow x \oplus e_{k}\)
    count \(\leftarrow A\left[c_{i}\right.\), buddy]
    if \(x_{k}=0\) then \(A\left[c_{i}, y\right] \leftarrow A\left[c_{i}, y\right] \quad\) if buddy is at higher index then
        + count
    else ( \(x_{k}=1\) )
    begin
        \(x \leftarrow\) buddy
        if count \(\neq 0\)
        buddy is head of the \(2^{k}\)-size
        block
    if buddy is active then we are
        not lowest ranked in this \(2^{k}\) -
        block
```

```
    then if flag \(=1\) then [flag -0 ;
        \(r \leftarrow k-1]\)
    eise null (eliminates "dangling else")
    else (count=0) buddy is inactive
    if flag \(=1\) then
    begin
        \(A\left[c_{i}, x\right] \leftarrow A\left[c_{i}, y\right]\)
        \(A\left[c_{i}, y\right] \leftarrow 0\)
        \(y \leftarrow x\)
    end (of then block)
end (of else \(x_{k}=1\) )
end (of for loop)
\(B\left[c_{i}\right] \leftarrow A\left[c_{i}, y\right] \quad y\) will be zero here
```

At this point we have isolated one representative of each number that appears among the numbers to be sorted and we have obtained a count of how many times each number occurs. We now will accumulate the counts (for each number $c_{i}$ ) of all numbers that are greater than $c_{i}$ in order to know the actual ranking of the numbers, assuming that duplicate numbers will be kept. This accumulation will be done in a manner similar to that used previously.

```
Algorithm 2.2-parallel bucket sort (part 2)
Input: from Algorithm 2.1
Output: \(\quad 0 \leq i \leq n-1 D[i]=\) sorted position of \(c_{i}\) only for the first
            instance of each \(c_{i}\)
                larger values of \(c_{i}\) will have smaller values of \(D=(\#\) of \(k\)
                        s.t. \(\left.c_{k}>\mathrm{c}_{i}\right)+1\)
for all \(i\) do
    \(D[i] \leftarrow 0\)
    if flag \(=1\) then
    begin Let \(c_{i}=w_{\text {og } m} \ldots w_{2} w_{1}\) be the binary representation of \(c_{i}\)
        flag2 \(\leftarrow 1\)
        \(w \leftarrow c_{i} \quad w\) is head of the largest block
        \(z \leftarrow\)
        \(z \leftarrow c_{i}\)
                            \(z\) is the location at which \(p_{i}\) is
        accumulating that count
                            \(B[z]\) now \(=\#\) of \(k\) s.t. \(c_{k}=z\)
        for \(k \leftarrow 1\) step 1 until \(\log m\) do
        begin
            buddy \(\leftarrow w \oplus e_{k}\)
            count \(\leftarrow B[\) buddy \(] \quad B[z]\) will \(=\#\) of \(k\) s.t. \(c_{k} \geq z\)
            if \(w_{k}=0\) then
                \(B[z] \leftarrow B[z]+\) count
            else ( \(w_{k}=1\) )
        begin \(w \leftarrow\) buddy
            if count \(\neq 0\) then
                    flag \(2 \leftarrow 0\)
            else if flag \(2=1\) then
        begin
            \(B[w] \leftarrow B[z]\)
                    \(B[z] \leftarrow 0\)
                    \(z \leftarrow w\)
            end (of then block)
            end (of else \(w_{k}=1\) )
        end (of for loop)
        \(D[i] \leftarrow B[z]-A\left[c_{i}, y\right]+1\)
        \(A\left[c_{i}, y\right] \leftarrow B[z]\)
        \(B[z] \leftarrow 0\)
    end (of if flag=1)
```

At this point, the representative of each number that appears among the $\left\{c_{i}\right\}$ has a count of the total number of $c_{t}$ 's that are greater than it plus the number of $c_{t}$ 's that
are equal to it. Each of the duplicates has a count of the number of $c_{t}$ 's that are equal to it but of higher index. The $D$-value, i.e. rank, is just the difference of these two quantities plus one.

The $D$-value of the representatives was calculated at the end of Algorithm 2.2 and so the $D$-values of the duplicates can be calculated by:

```
for all ido
    if flag = 0 then D[i]}\leftarrowA[\mp@subsup{c}{i}{},0]-A[\mp@subsup{c}{i}{},y]+
```

If we insist that not more than one processor may simultaneously access a location, not even for fetches, then the $D$-values of duplicates can be evaluated using the reverse of the procedure of Algorithm 2.1. This is done below in Algorithm 2.3.

```
Algorithm 2.3-parallel bucket sort (part 3)
Input: from Algorithm 2.2
Output: \(\quad 0 \leq i \leq n-1 A[j, i]=0\)
        \(0 \leq j \leq m-1 D[i]=\) sorted position of \(c_{i}\)
                        \(=\#\) of \(k\) s.t. \(c_{k}>c_{i}\)
                        \(+\#\) of \(k \leq i\) s.t. \(c_{k}=c_{i}\)
        larger values of \(c_{i}\) will have smaller values of \(D\)
        equal values of \(c_{i}\) will keep relative order
for all ido
    for \(k \leftarrow(\log n)-1\) step -1 until 0 do
    if \(r=k\) then
    begin
        get value of \(A\left[c_{i}, 0\right]\) from buddy
    \(x \leftarrow y \oplus e_{r}\)
        location
    \(D[i] \leftarrow A\left[c_{i}, x\right]-A\left[c_{i}, y\right]+1\)
    \(A\left[c_{i}, y\right] \leftarrow A\left[c_{i}, x\right]\)
    end
    else if \(r>k\) then
        begin
            \(x \leftarrow y\) OR (iAND \(\left.e_{k}\right)\)
            if \(x \neq y\) then begin
                    \(A\left[c_{i}, x\right] \leftarrow A\left[c_{i}, y\right]\)
                    \(A\left[c_{i}, y\right] \leftarrow 0\)
                    end (of then block)
        end (of if \(r>k\) )
    (end of for loop)
    \(A\left[c_{i}, y\right] \leftarrow 0\)
```

It is noted that Algorithm 2 (the sequence of Algorithms $2.1,2.2,2.3$ ) requires space $S=O(m n)$, time $T=O(\log n+\log m)$, and the use of $n$ processors.

The algorithms given above assume that an area ( $A$ ) of memory has been initialized to zero. This is not unreasonable. Many instances of this algorithm can be executed one after another. The memory will be clear upon the termination of each program.

However, there are methods which make it unnecessary to initialize the area. For serial programs, one can include at each location a pointer to a backpointer on a stack. Each time an entry is accessed, verification can be made that the contents are not random by checking that the pointer in that entry points to the active region on the stack and that the backpointer points to the entry [1].

This method is also valid for parallel programs unless we add the restriction that simultaneous multiple access
to the same location is prohibited even for memory-fetch instructions. It is possible that several entries that are accessed in parallel may have random contents, more than one of which points to the same location. A memory fetch conflict would ensue.

For situations similar to that in Algorithm 2, the following is a possible solution. At each step of the process, each active processor can initialize the location its buddy is working on (to zero), then reinitialize the contents of the location it is working on (to its latest value). A location will thus be initialized if either of the two processes to which it might be relevant is active.

We now present algorithms that will sort $n$ arbitrary numbers in time $O(\log n)$. They are based on an extension of an algorithm due to Gavril [8] that merges two linearly-ordered sets in time $O(\log n)$. Our first algorithm to do this, Algorithm 3, will require the use of $n^{3 / 2}$ processors.
Algorithm 3-parallel sort using $n^{3 / 2}$ processors
Input: $0 \leq i \leq n-1$
$c_{i} \in$ integers
Output: $\quad\left\{c_{i}\right\}$ will be stably sorted, smallest first

1. Partition the $n$ input numbers into $n^{1 / 2}$ groups, each having $n^{1 / 2}$ elements.
2. Within each group do

For each element, $j$, determine count $[j]=$ (\# of $i$ such that $\left.c_{i}<c_{j}\right)+$ (\# of $i \leq j$ such that $c_{i}=c_{j}$ ). This can be done in time $O(\log n)$ using $n^{1 / 2}$ processors per element (a total of $n$ processors per group or $n^{3 / 2}$ processors in toto). The $n^{1 / 2}$ processors for element $j$ will be assigned, one to each element $i$ in $j$ 's group, to compare $c_{i}$ with $c_{j}$. Summing the results of these comparisons can be done in time $O(\log n)$.
3. Within each group do

Bucket sort the elements, using count [ j ] as the key for the $j$ th element in the group. This is done by: $c_{\text {count } / \beta} \leftarrow c_{j}$, where $j$ and count $[j]$ are offsets (of value at most $n^{1 / 2}$ ) from the beginning of each group. There will be no memory conflicts since the count $[j]$ 's within a group are all distinct. Steps 2 and 3 have effectively sorted the elements within each group using an "Enumeration Sort" [11]. 4. All elements do a binary search of the $n^{1 / 2}$ groups. That is, each element $\left(c_{j}\right)$ in group $g$, has $n^{1 / 2}$ processors which are assigned, one to each group, to do a binary search on the elements in a group (which are sorted) so as to determine, for all groups $k$, the value of

$$
\begin{aligned}
\operatorname{count}[j, k]= & \text { if } k<g, \# \text { of elements } i \text { such that } c_{i} \leq c_{j} \\
& \text { if } k=g, j \\
& \text { if } k>g, \nRightarrow \text { of elements } i \text { such that } c_{i}<c_{j}
\end{aligned}
$$

where $c_{i}$ refers to the $i$ th element in group $k$ and $c_{j}$ is fixed.
5. For all elements, $j$, evaluate count $[j]=$ sum (over $k$ ) of count$[j, k]$. This can be done in time $O(\log n)$ and requires $n^{1 / 2}$ processors per element for a total of $n^{3 / 2}$ processors.
6. Do a bucket sort on all $n$ elements using count $[j]$ as the key for the $j$ th element. Again, there will be no memory conflicts since count $[j]$ will be the rank of the $j$ th element.
7. END of Algorithm 3.

We note that Algorithm 3 requires time $O(\log n)$ and the use of $n^{3 / 2}$ processors. We now show a simple modification of Algorithm 3 which will use the same order of magnitude of time and require only $n^{4 / 3}$ processors.
Algorithm 4-parallel sort using $n^{4 / 3}$ processors

1. Partition the $n$ input numbers into $n^{2 / 3}$ groups each having $n^{1 / 3}$ elements.
2. Within each group do

For each element, $j$, determine count $[j]=\#$ of $i$ such that $c_{i}<\mathrm{c}_{j}$ )
$+\left(\#\right.$ of $i \leq j$ such that $\left.c_{i}=c_{j}\right)$.
3. Within each group do

Bucket sort the count $[j]$ 's obtained in step 2. This will rearrange the elements in rank order within each group.
4. Divide the $n^{2 / 3}$ groups into $n^{1 / 3}$ sectors, each sector consisting of $n^{1 / 3}$ groups.
5. Within each sector do

For each element ( $j$ ) in group $g$, do a binary search of each of the $n^{1 / 3}$ groups in $j$ 's sector to determine, for all $k$, the value of

$$
\begin{aligned}
\operatorname{count}[j, k]= & \text { if } k<g, \# \text { of } i \text { in group } k \text { such that } c_{i} \leq c_{j} \\
& \text { if } k=g, j \\
& \text { if } k>g, \# \text { of } i \text { in group } k \text { such that } c_{i}<c_{j}
\end{aligned}
$$

Then, for each element $j$, evaluate count $[j]=$ (\# of $i$ in $j$ 's sector such that $c_{i}<c_{j}$ ) + (\# of $i \leq j$ in $j$ 's sector such that $c_{i}=c_{j}$ ). This number is simply the sum (over $k$ ) of count $[j, k]$.
6. Within each sector, do a bucket sort of the elements within the sector using count $[j]$ as they key for element $j$. This will rearrange the elements in rank order within each sector.
7. For all elements $(j)$ in sector $t$, do a binary search of each of the $n^{1 / 3}$ sectors to determine, for all $k$, the value of

$$
\begin{aligned}
\operatorname{count}[j, k]= & \text { if } k<t, \# \text { of } i \text { in sector } k \text { such that } c_{i} \leq c_{j} \\
& \text { if } k=t, j \\
& \text { if } k>t, \# \text { of } i \text { in sector } k \text { such that } c_{i}<c_{j} .
\end{aligned}
$$

Then evaluate count $[j]=$ the sum (over $k$ ) of $\operatorname{count}[j, k]$.
8. Do a bucket sort of all $n$ elements.
9. END of Algorithm 4.

In a like manner, we can exhibit an algorithm to sort $n$ numbers in $O(k \log n)$ time that uses $n^{1+1 / k}$ processors. Interestingly, by setting $k=\log n$ (initially splitting the $n$ elements into $n / 2$ groups of 2 each), we obtain an algorithm to sort $n$ numbers in $O\left(\log ^{2} n\right)$ time using $\mathrm{O}(n)$ processors, the same resources used by Batcher's algorithms.

We note that these algorithms, although avoiding memory-store conflicts, do have memory-fetch conflicts. That is, we allow more than one processor to simultaneously access the same memory location. As an open problem, we pose the question: Can $n$ numbers be sorted in time $O(\log n)$ if memory-fetch conflicts are not permitted?

## Received December 1976; revised September 1977

## References

1. Aho, A.V., Hopcroft, J.E., and Ullman, J.D. The Design and Analysis of Computer Algorithms. Addison-Wesley, Reading, Mass., 1973, p. 71.
2. Arjomandi, E. A study of parallelism in graph theory. Ph.D. Th., Dept. of Comptr. Sci., U. of Toronto, Toronto, Ont., Dec. 1975. 3. Batcher, K.E. Sorting networks and their applications. Proc. AFIPS 1968 SJCC, Vol. 32, AFIPS Press, Montvale, N.J., pp. 307-314.
3. Brent, R.P. The parallel evaluation of general arithmetic expressions. J. ACM 21, 2 (April 1974), 201-206.
4. Csansky, L. Fast parallel matrix inversion algorithms. Proc. 16th Annual Symp. on Foundations of Comptr. Sci., IEEE, Berkeley, Calif., Oct. 1975, pp. 11-12.
5. Even, S. Parallelism in tape-sorting. Comm. ACM 17, 4 (April 1974), 202-204.
6. Flynn, M.J. Very high-speed computing systems, Proc. IEEE 54 (Dec. 1966), 1901-1909.
7. Gavril, F. Merging with parallel processors. Comm. ACM 18, 10 (Oct. 1975), 588-591.
8. Hirschberg, D.S. Parallel algorithms for the transitive closure and the connected component problems. Proc. 8th Annual ACM Symp. on Theory of Comptng. Hershey, Pa., May 1976, pp. 55-57.
9. Knuth, D.E. The Art of Computer Programming, Vol. 1 : Fundamental Algorithms. Addison-Wesley, Reading, Mass., Sec. Ed., 1973.
10. Knuth, D.E. The Art of Computer Programming, Vol. 3: Sorting and Searching. Addison-Wesley, Reading, Mass., 1973.
11. Levitt, K.N., and Kautz, W.H. Cellular arrays for the solution of graph problems. Comm. ACM 15, 9 (Sept. 1972), 789-801.
12. Muller, D.E., and Preparata, F.P. Bounds to complexities of networks for sorting and for switching. J. ACM 22, 2 (April 1975), 195-201.
13. Munro, I., and Paterson, M. Optimal algorithms for parallel polynomial evaluation, J. Comptr. Syst. Sci. 7 (1973), 189-198.
14. Muraoka, Y., and Kuck, D.J. On the time required for a sequence of matrix products. Comm. ACM 16, 1 (Jan. 1973), 22-26.
15. Stone, H.S. Parallel processing with the perfect shuffle. IEEE Trans. Comptrs. C-20 (1971), 153-161.
16. Thompson, C.D., and Kung, H.T. Sorting on a mesh-connected parallel computer. Proc. 8th Annual ACM Symp. on Theory of Comptng. Hershey, Pa., May 1976, pp. 58-64.
17. Valiant, L.G. Parallelism in comparison problems. SIAM J. Comptng. 4, 3 (Sept. 1975), 348-355.

Communications
of the ACM

August 1978
Volume 21
Number 8


[^0]:    General permission to make fair use in teaching or research of all or part of this material is granted to individual readers and to nonprofit libraries acting for them provided that ACM's copyright notice is given and that reference is made to the publication, to its date of issue, and to the fact that reprinting privileges were granted by permission of the Association for Computing Machinery. To otherwise reprint a figure, table, other substantial excerpt, or the entire work requires specific permission as does republication, or systematic or multiple reproduction.

    Research supported by NSF grant MCS-76-07683.
    Author's present address: Department of Electrical Engineering, Rice University, Houston, TX, 77001.
    (C) 1978 ACM 0001-0782/78/0800-0657 \$00.75

