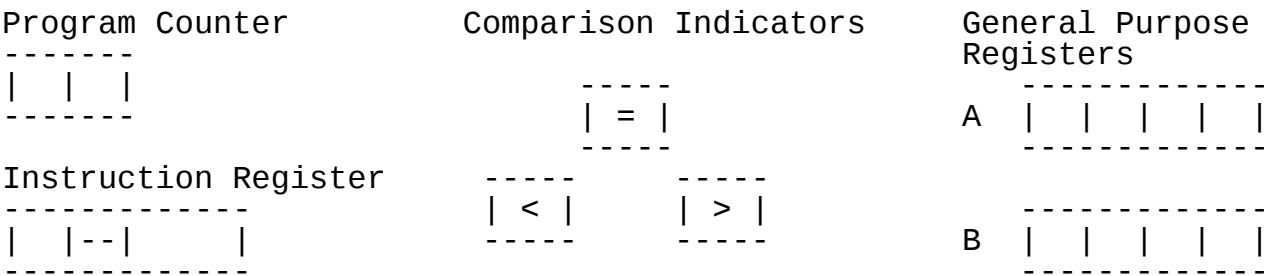


ARCHITECTURE OF THE
DEUS X



MAIN MEMORY			
Address	Contents	Address	Contents
0	----- -----	20	----- -----
1	----- -----	21	----- -----
2	----- -----	22	----- -----
3	----- -----	23	----- -----
4	----- -----	24	----- -----
5	----- -----	25	----- -----
6	----- -----	26	----- -----
7	----- -----	27	----- -----
8	----- -----	28	----- -----
9	----- -----	29	----- -----
10	----- -----	30	----- -----
11	----- -----	31	----- -----
12	----- -----	32	----- -----
13	----- -----	33	----- -----
14	----- -----	34	----- -----
15	----- -----	35	----- -----
16	----- -----	36	----- -----
17	----- -----		----- . . . -----
18	----- -----	65534	----- -----
19	----- -----	65535	----- -----

ARCHITECTURE OF THE DEUS X

(Snapshot of machine status before executing first class example)

Program Counter

```
-----
| | |
-----
```

Comparison Indicators

```
-----
| = |
-----
```

General Purpose
Registers

```
-----
A | | | | |
-----
```

Instruction Register

```
-----
| | -- | |
-----
```

```
-----
| < | | > |
-----
```

```
-----
B | | | | |
-----
```

MAIN MEMORY

Address

Contents

0	0
1	0
2	0
3	0
4	5 1000
5	0
6	0
7	0
8	40 5
9	41 1002
10	9 2000
11	10 1000
12	3 1001
13	21 2006
14	6 2000
15	0
16	8
17	
18	

. . .

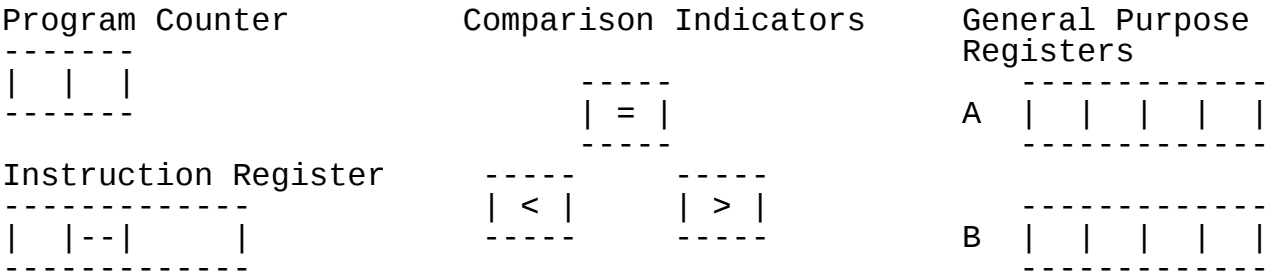
Address

Contents

999	
1000	
1001	
1002	
1003	
1004	
1005	
1006	
	. . .
1019	
	. . .
2000	
2001	
2002	
2003	
2004	
2005	
2006	
	. . .
2029	

ARCHITECTURE OF THE
DEUS X

(Snapshot of machine status before executing second class example)



MAIN MEMORY			
Address	Contents	Address	Contents
0	0	999	
1	0	1000	
2	0	1001	
3	0	1002	
4	5 1000	1003	
5	10 1002	1004	
6	50 999	1005	
7	60 16	1006	
8	40 5		. . .
9	41 1002	1019	
10	9 2000		. . .
11	10 1000	2000	
12	3 1001	2001	
13	21 2006	2002	
14	6 2000	2003	
15	7 4	2004	
16	8	2005	
17		2006	
18			. . .
	. . .	2029	
			. . .