CS143A Principles on Operating Systems Discussion 09:

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Dec 6, 2019 **1pm**

Agenda

- Implementing new system calls
- Threads

Files to modify for new system calls



Threads

- Lightweight Process (LWP)
 - They share address space
 - We don't create new pages—faster creation
- Inter-process communication is costly:
 - Through file
 - Shared memory (much complicated than thread's)
 - pipe
 - socket
 - ...
- Faster context-switching



create process



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allocate user virtual memory & copy pages (we don't need this)



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*np->tf = *curproc->tf;
// Clear %eax so that fork returns 0 in the child.
np->tf->eax = 0;
for(i = 0; i < NOFILE; i++)</pre>
  if(curproc->ofile[i])
    np->ofile[i] = filedup(curproc->ofile[i]);
np->cwd = idup(curproc->cwd);
safestrcpy(np->name, curproc->name, sizeof(curproc->name));
pid = np->pid;
acquire(&ptable.lock);
np \rightarrow state = RUNNABLE;
release(&ptable.lock);
return pid;
```

return value.. where does eip point to now?

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kind of a spinlock schedule the thread

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np->state = RUNNABLE;

release(&ptable.lock);

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22 }

```
struct trapframe {
    // registers as pushed
    uint edi;
    uint esi;
    uint ebp;
    uint oesp; // usel
    uint ebx;
    uint edx;
    uint ecx;
    uint eax;
```

// rest of trap frame

ushort gs; ushort padding1; ushort fs; ushort padding2; ushort es; ushort padding3; ushort ds; ushort padding4; uint trapno;

// below here defined
uint err;
uint eip;
ushort cs;
ushort padding5;
uint eflags;

// below here only whe
uint esp;
ushort ss;
ushort padding6;

return value.. where does eip point to now?

copy file descriptors

- 1. set eip
- 2. set esp (stack top)
- 3. push arguments and return address to stack

kind of a spinlock

schedule the thread

Amdahl's Law



- A program consists of two parts: parallelizable vs. non-parallelizable(serial)
- If parallelizable takes 60% of execution time, even if we use infinite number of threads to parallelize it, the program's execution time is reduced by 60%
- maximum speedup 1/0.4 = 2.5x



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Race condition (Data race)

Parent

a=0;

 Thread #1
 Thread #2

 b=a++;
 b=a++;

Thread 1	Thread 2	Memory Value
read		0
increment		0
write		1
	read	1
	increment	1
	write	2

Thread#1's b: 1 Thread#2's b: 2

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write		1

Thread#1's b: 1 Thread#2's b: 2 Thread#1's b: 1 Thread#2's b: 1

Parent

a=0;

 Thread #1
 Thread #2

 b=a++;
 b=a++;

Process 1	Process 2	Memory Value
atomic_inc		1
	atomic_inc	2

atomic_inc = read + inc + write

Parent a=0;

Thread #1	Thread #2
b=a++;	b=a++;

Process 1	Process 2	Memory Value
atomic_inc		1
	atomic_inc	2

• Special instructions

• e.g. xchg

temp = a;	
a = b:	temp = a;
h – tomn:	a = b;
b – temp,	b = temp;

atomic_inc = read + inc + write

Parent a=0;

1 Thread #2

Thread #1 b=a++;

b=a++;

Process 1	Process 2	Memory Value
atomic_inc		1
	atomic_inc	2

atomic_inc = read + inc + write

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- Spinlock uses CPU continuously
- It will take portion of CPU utilization
- Degrades performance of other threads/process

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void Lock()
{
    while (lock ==1); >
    lock = 1;
}
void Unlock()
{
    lock = 0;
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- Degrades performance of other threads/process
- Why don't we make it sleep while waiting?

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- It will take portion of CPU utilization
- Degrades performance of other threads/process
- Why don't we make it sleep while waiting?
 → Mutex (mutually exclusive)
- xv6 doesn't have yield() so we use sleep() to mimic the behavior















sem_init: initialize counter
(~ # of books)

sem_wait: if counter > 0,
decrease by 1
if counter == 0, wait until it is
greater than 0

sem_post: increase counter
by 1

HINT: counter should be in critical section. You can also use cond var









Per-thread Variables

- Local variables will be on stack..
- its address points to somewhere in the thread's stack