

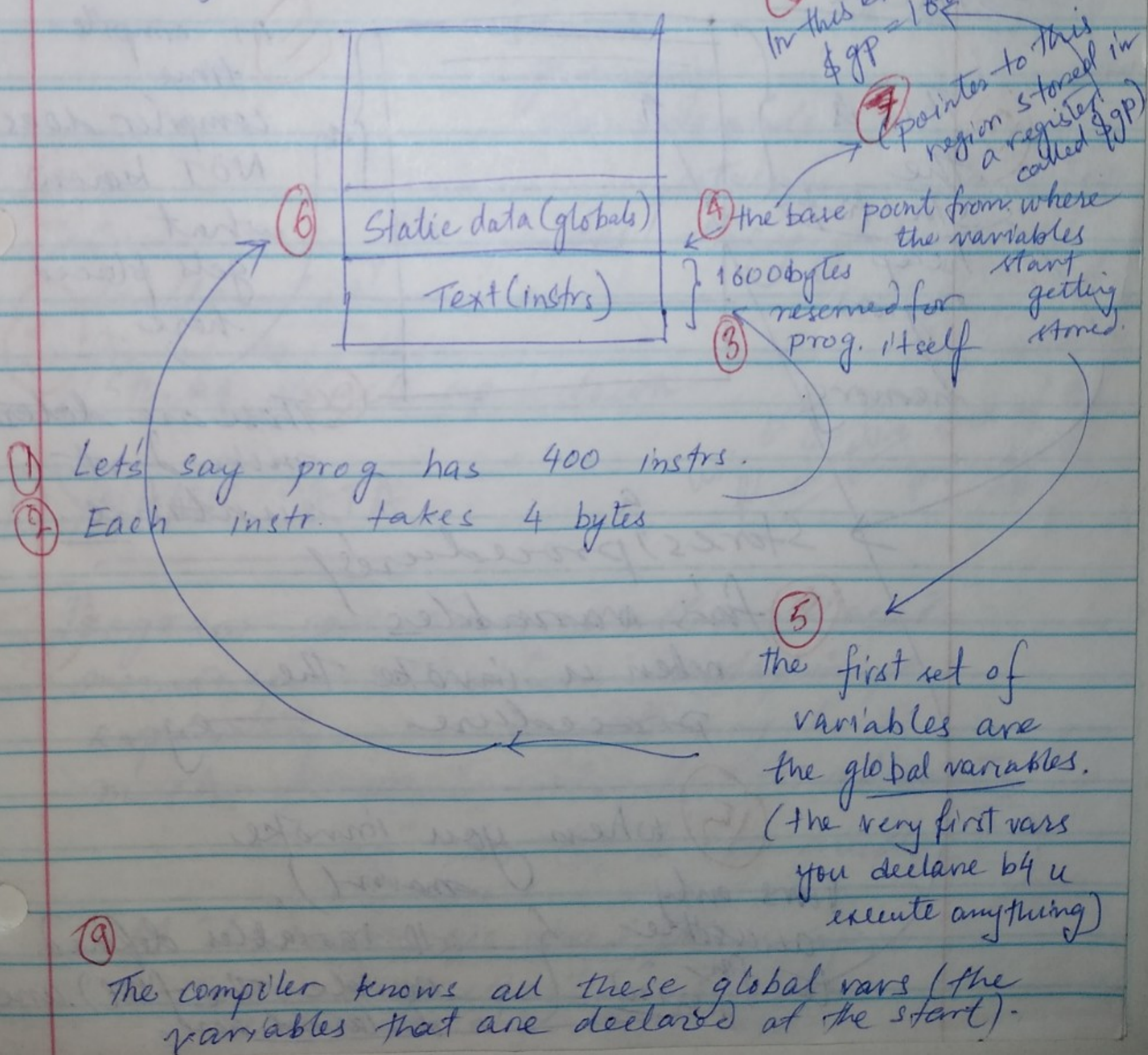
# Memory Organisation

FALL 19 CS250P  
Discussion-02  
14<sup>th</sup> Oct 2019

Last time,

Register file in processor, with 32 registers

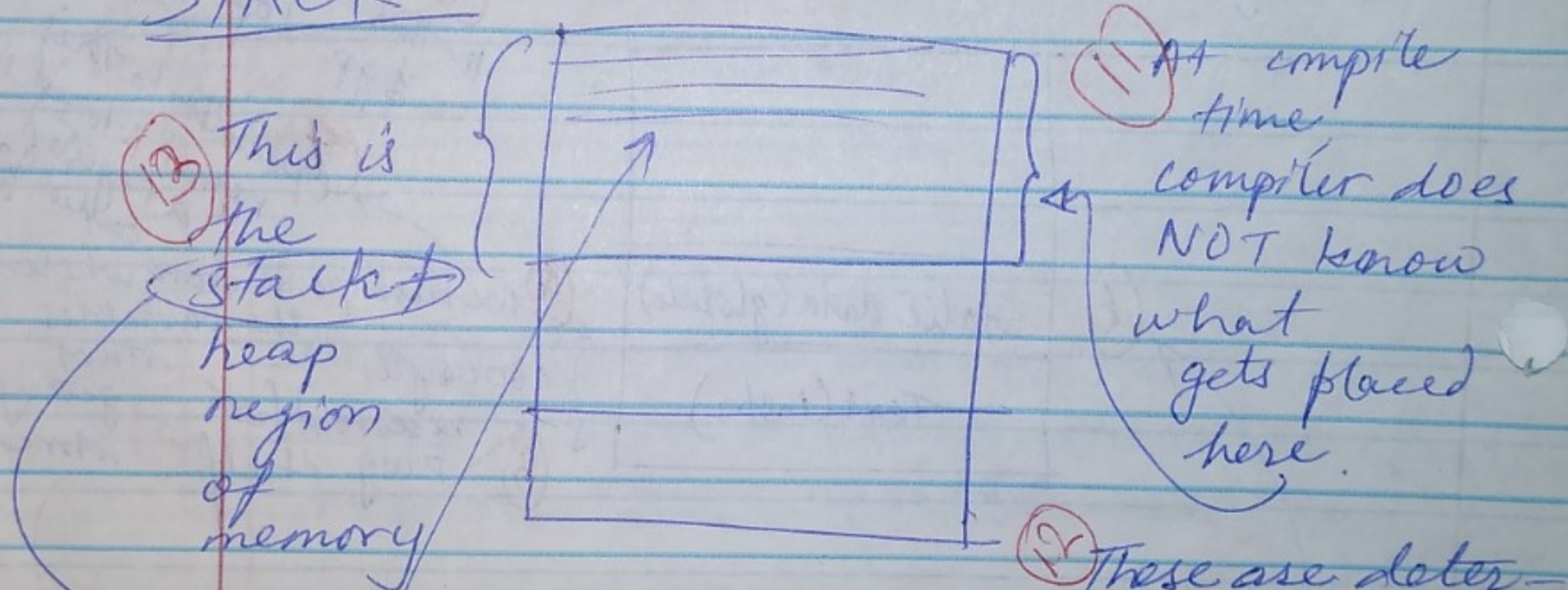
The program has access to a larger memory.



9) These variables get allocated beforehand, in the global region.  
(before runtime, during execution)

10) From that point on, variables are allocated as per need.

## STACK



13) This is the stack heap region of memory

14) At compile time compiler does NOT know what gets placed here.

14) These are determined at runtime.

Stores procedures

14) fn's variables when u invoke the procedure — eg.

15) when you invoke main(), vars only available to fn main. all variables defined inside the fn() are declared here.

main() <sup>may call</sup> → find()

those its variables

get declared here.

low top of the stack growing downwards)

Keeping track of stack is done by the help of 2 pointers.

\$SP, \$FP

start of the procedure

end of the procedure space

① Once the fn finishes, they get deallocated.

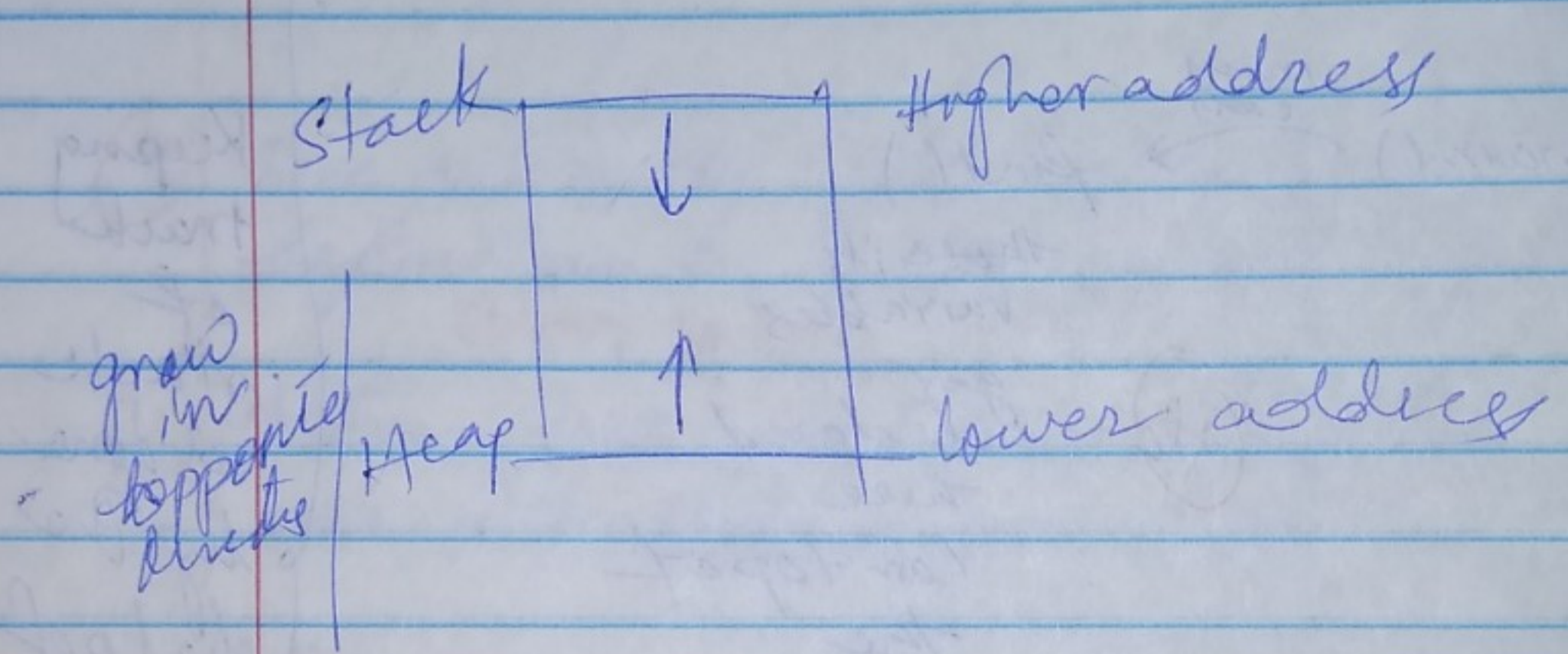
② In this way, the stack moves up & down

we use fn example

## DYNAMIC DATA

① Program can dynamically allocate memory — where it defines explicitly — i.e. defining how much memory to allocate to allocate.  
e.g. in C, we use malloc()

These vars are stored in heap, & the heap starts where the global variables storage end.



Base address & offsets

E.g. C code,

```

a = b + c;
...
int a, b, c, d[10]
main()
  ↑
  a = b + c
  d[3] = d[2] + a;
  
```

Let's say prog. had 250 instructions,  
 4 bytes each  
 ↓  
 code region  
 eats 1000 bytes  
 of space

So global variable starts at address 1000,

So, I do, add 1000 to global pointer

~~\$50~~ ~~\$59~~  $\text{addi } \$gp, \$zero, 1000$

need to load b & c,

generate instructions

store new values

$\text{lw } \$s2, 4(\$gp)$

$\text{lw } \$s3, 8(\$gp)$

$\text{add } \$s1, \$s2, \$s3$

$\text{sw } \$s1, \$gp$

$\text{addi } \$s4, \$gp, 12$

$\text{lw } \$t0, 8(\$s4) \# \text{ get } d$

$\text{add } \$t0, \$t0, \$s1$

$\text{sw } \$t0, 12(\$s4)$

reg		from \$gp
\$s1	a	0 from \$gp
\$s2	b	4 from \$gp
\$s3	c	8 from \$gp
\$s4	d	12

## Instruction Formats (Have Instr. format slide)

2 Broad classes of instructions

~~R-type~~

last 6 bits denotes the variation in adding (adding a byte, 2 bytes, or a word)

(the 5 bits shift is used for operation instructions like,

sl

srl

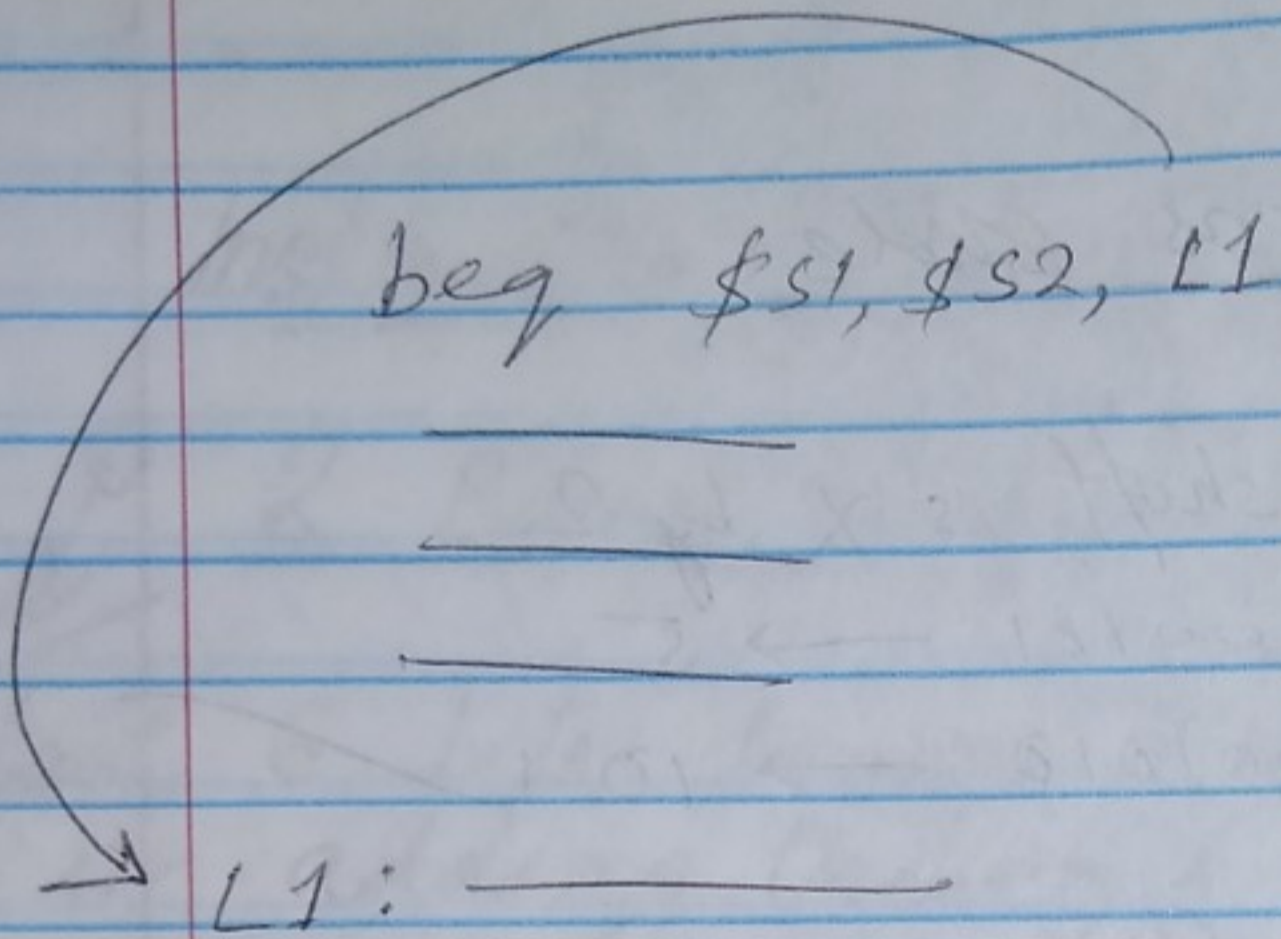
by which where bits are shifted to the left or right.

These bits tell us how much this shift is.

~~I-type~~

e.g. immediate,  
lw, sw

Have  $R_i$  5 bits  $\rightarrow$  1st reg. operand  
5 bits  $\rightarrow$  next reg. operand  
16 bits  $\rightarrow$  stores the offsets.



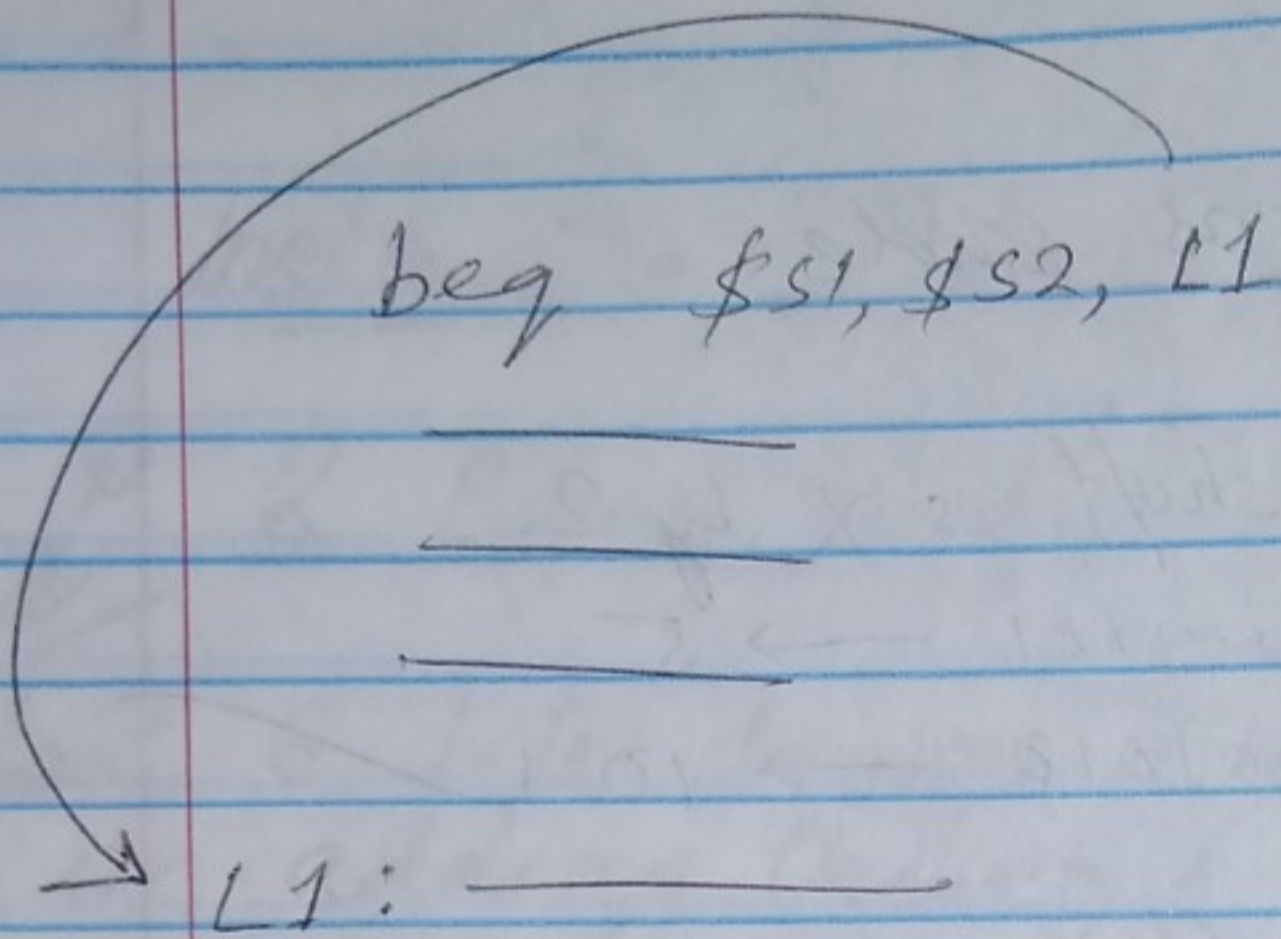
bneq

se 'slt → ⊗ not itself a branch instruction  
(set on less than) ⊗ sets a certain value based on a comparison of 2 registers.

~~eg~~ slt \$t0, \$f1, \$f2  
set \$t0 to 1 if \$f1 < \$f2

can be used with a follow up branch instruction which checks result of slt instruction, & proceeds accordingly

2nd kind: Unconditional branch:  
jr → jump to address in a register.



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Task (code)

```
if (i == j)
    f = g + h;
else
    f = g - h;
```

[Assembly]  
Say \$s1 & \$s2  
have i & j

[explain  
side]

```
while (save[i]
       == k)
    i += 1;
```

Say, i, k, same base are  
in \$s3, \$s5, \$s6

Ans:

A ~~label~~ can also be  
an instruction (an empty  
instruction) assembler/  
compiler will read it  
as a jump location.

Loop:

P.T.O

```

Loop:  su $t1, $s3, 2
      add $t1, $t1, $s6
      lw  $t0, 0($t1)
      bne $t0, $s5, Exit
      addi $s3, $s3, 1

```

① jump to the top of the loop  
 Exit: j Loop

save[0] → ist in \$s6    ② get the  
 save[1] → " " \$s6 + 4 value in  
 save[2] → " " \$s6 + 8 save[i]

save[i] →  $\boxed{\$s6 + 4 \times i}$     ③

↓  
 shift i by 2 to the left    ④